

Towards Highly Scalable Clusters for Crash

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IBM Deep Computing Team

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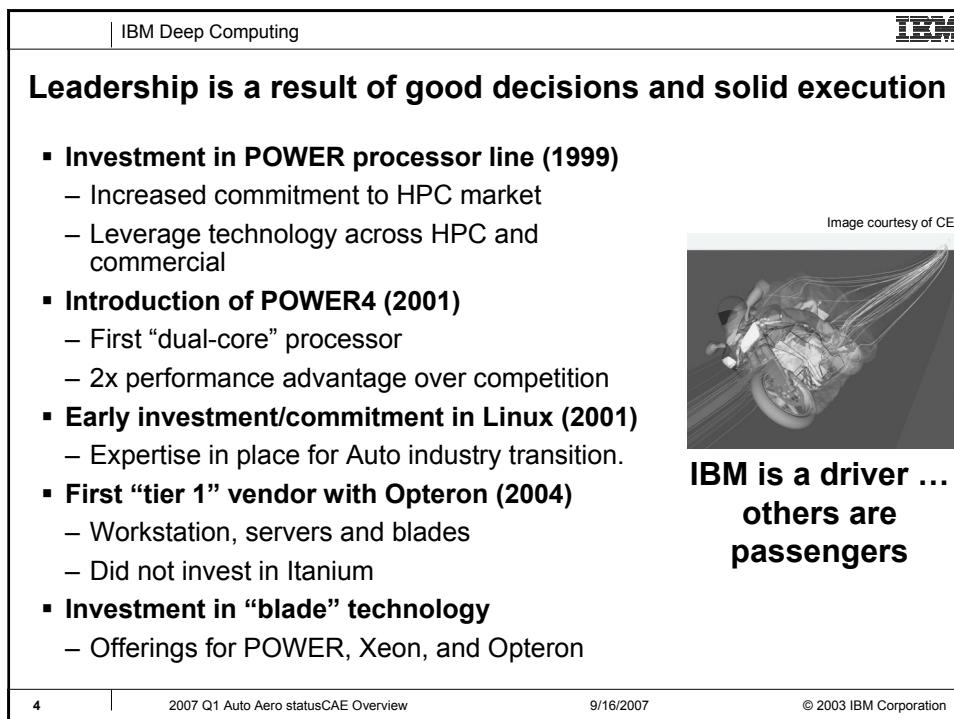
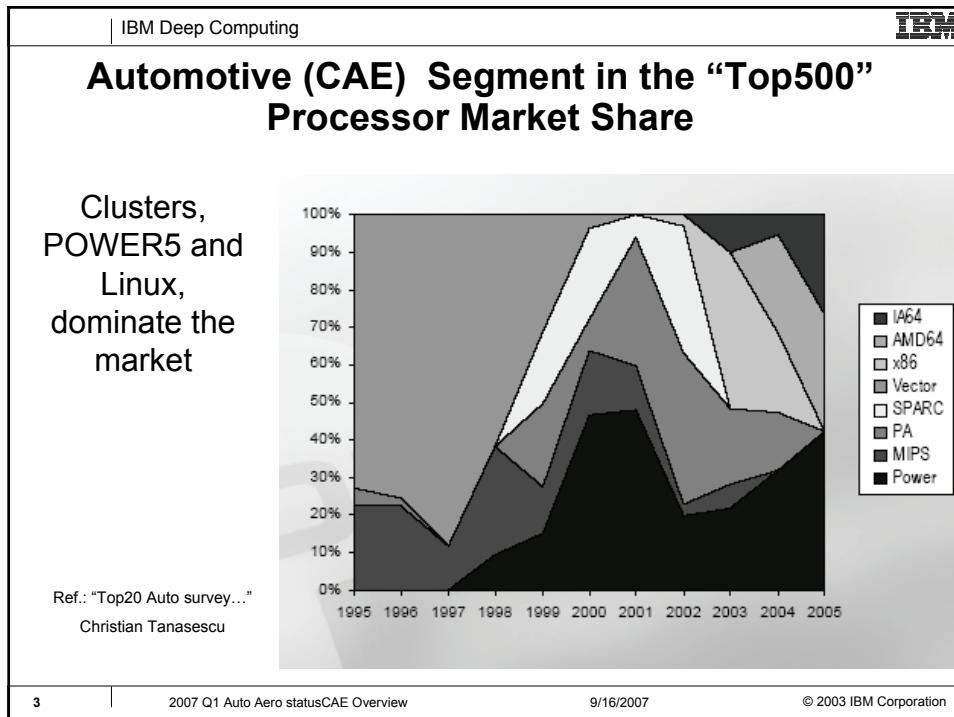
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- **Industry Trends**
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- **HPC Product Update**
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 - Opteron “rev F”
- **IBM “value add”**
 - GPFS
 - vMIO
 - 10M element crash
- **General Discussion**

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Evolution of HPC Hardware

The diagram illustrates the progression of High-Performance Computing (HPC) hardware over three decades:

- MainFrames (~1979)**: Represented by a server rack icon.
- Vectors (~1983)**: Represented by a server rack icon.
- RISC SMPs (~1994)**: Represented by a server rack icon.
- Clusters (~2001)**: Represented by a server rack icon.
- Embedded (now)**: Represented by a server rack icon.

Annotations provide historical context:

- MainFrames (~1979)**: "Beginning in 1986 crash simulation drove CAE compute requirements".
- Vectors (~1983)**: "SMP architecture was often first introduced in the CFD department and helped push parallel computing".
- RISC SMPs (~1994)**: "Cluster architecture (Unix & Linux) now dominate crash and CFD environment".
- Clusters (~2001)**: "Embedded systems show new perspectives for CAE".

Bottom notes: Mostly MSC.Nastran, 5, 2007 Q1 Auto Aero status, 9/16/2007, © 2006 IBM Corporation

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CAE System Architecture: AIX and/or Linux

- IBM business is about 50/50 Linux clusters and AIX clusters
- Linux systems tend to be special purpose
- AIX/Power systems are preferred for “implicit structures” and general purpose systems

Bottom notes: 6, 2007 Q1 Auto Aero status, 9/16/2007, © 2006 IBM Corporation

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Challenges of Clusters

- Applications that do not scale
- Cluster nodes have weak I/O (compared to large SMP)
- Parallel I/O across the cluster
- Lots of processors generate lots of heat.
- Lots of processors generate lots of data

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Challenges of Clusters

- Applications that do not scale
 - IBM value: POWER5/6 processor and “fast” x86 MSC.Nastran
- Cluster nodes have weak I/O (compared to large SMP)
 - IBM value: MIO for Linux (fast IO libraries).
- Parallel I/O across the cluster
 - IBM value: GPFS (General Parallel File System)
- Lots of processors generate lots of heat.
 - IBM value: “blades”, “Power Executive”, water cooled rack.
- Lots of processors generate lots of data
 - IBM value: working with ISVs to promote simulation data management

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PROCESSORS

Processor landscape becoming 'simplified'

- down to x86 and POWER

POWER

- first to dual-core
- POWER6 to push clock
- increasing reliance of SMT to maximize performance
- POWER family (i.e. embedded, gaming) influence on future

x86

- initial push to higher clocks
- thermal problems push direction to multi-core
- increasing reliance on SSE for HPC
- memory future is blurry (FBdimm vs DDR?)

Commonality

- ultimately the measure of performance will be dictated by the speed and number of threads per socket.

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POWER

Compute center economics paradigm shift

- worldwide demand for energy is increasing faster than supply
- as a result, energy consideration will become increasingly important factor in providing CAE server solutions
- while BG/L currently has limited applicability within CAE, its technology is pushing the envelope of energy efficiency, which will play a crucial role for future servers

Power and cooling spend will exceed new server spending (Gartner 2006)

Year	New server spending	Power and cooling
1996	~\$10	~\$5
1997	~\$12	~\$6
1998	~\$15	~\$7
1999	~\$18	~\$8
2000	~\$22	~\$10
2001	~\$25	~\$12
2002	~\$28	~\$14
2003	~\$32	~\$16
2004	~\$35	~\$18
2005	~\$38	~\$20
2006	~\$42	~\$22
2007	~\$45	~\$25
2008	~\$48	~\$30
2009	~\$52	~\$35

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- Costs have become an overriding consideration in packaging
 - product cycles changing from 36 to 6 months!
 - reuse of components is a must
- Environmentals (space and energy) pushing packaging technology forward
- Utility mentality emerging
 - shift from homogenous computer floor to constant upgrade of resource grid.

Frames
↓
Racks
↓
Blades
↓
Bricks




PACKAGING

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HPC Hardware Value

It is now much more than \$/MFLOPS

Total Cost of Ownership (TCO) is now more complicated.

- ISV application cost
- Power and Cooling
- Engineer productivity
- Data center floor space

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 - CFD
 - Interconnects
- IBM "value add"
 - PowerExecutive, GPFS
 - MD Nastran Tuning, vMIO, Accuracy
- General Discussion

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XEON vs. Opteron Product Positioning

- The benchmark wars are in full swing
 - Intel 5160 "Woodcrest 3.0 GHz
 - AMD/Opteron "Rev. F" 2.8 GHz
 - dual-core chips with comparable performance
- It is often difficult to identify optimal product to deploy
- There are several key things to understand about each solution that help us identify which is optimal for a given workload
- But remember, the areas where there are overwhelming and compelling differences between the two (Xeon and Opteron) are usually easily identified
 - and in many cases it boils down to customer preference

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New System x™ and BladeCenter® Servers: Intel Xeon processors



Position

x3550	x3650	X3850/3950	Ultimate scale-out integration
			
<i>Low cost HPC compute node</i>	<i>Highly available application server</i>	<i>Mid-Market, Large Enterprise HPC</i>	<i>Enterprise class scalable 2-socket blades for front and mid tier applications</i>

Key Features

<ul style="list-style-type: none"> Dual socket XEON 5100 series processors 1/32GB of FBD memory 2(3.5") or 4(2.5") SAS internal storage 	<ul style="list-style-type: none"> Dual socket XEON 5100 series processors 1/48GB of FBD memory 8(2.5") or and 6(3.5") SAS + tape internal storage 	<ul style="list-style-type: none"> Four socket XEON per node 16 2/64GB of DDR2 per node Up to 8 nodes per system 6(2.5") SAS Internal storage per node 	<ul style="list-style-type: none"> Dual socket XEON 5100 series processors 1/32GB of FBD memory 2(2.5") SAS internal storage + 3(2.5") with optional SIO blade
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New System x™ and BladeCenter® Servers: AMD Opteron™ Rev F



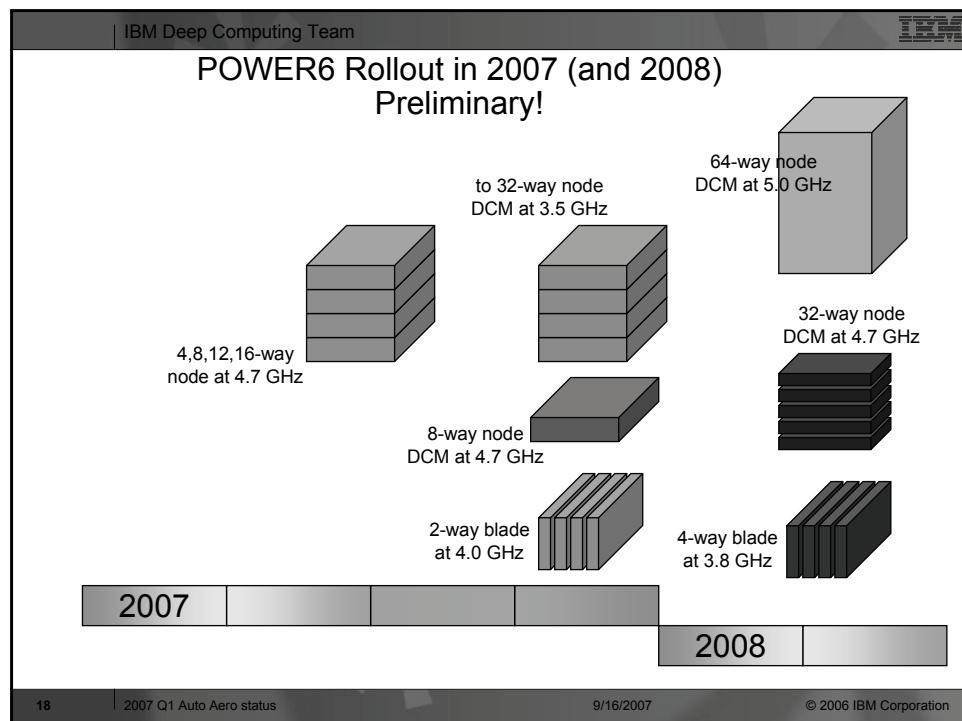
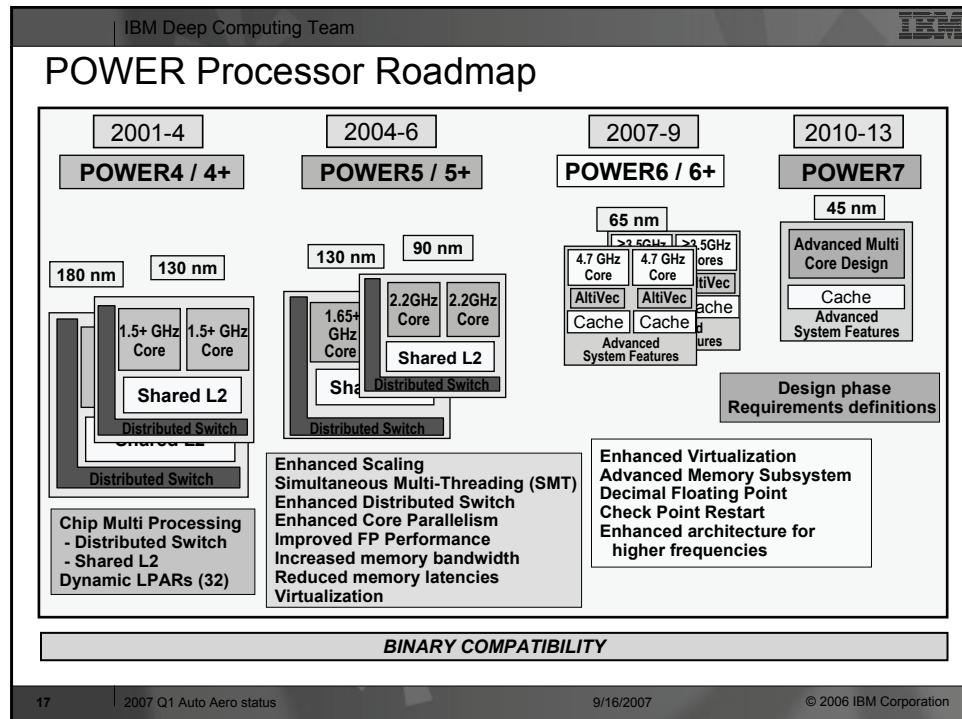
Position

x3455	x3655	x3755	Ultimate scale-out integration
			 
<i>Low cost HPC compute node</i>	<i>Highly available application server</i>	<i>Mid-Market, Large Enterprise HPC</i>	<i>Enterprise class scalable 2-4 socket blade for front and mid tier applications</i>

Key Features

<ul style="list-style-type: none"> Dual socket Opteron processors 48GB of DDR2 memory 3.5" Fixed SATA Leadership I/O with PCI-E, and HTx 	<ul style="list-style-type: none"> Dual socket Opteron processors 64GB of DDR2 memory 2.5 and 3.5" internal storage and tape Ready RAID and Ready RSA Trusted Platform Module Standard TOE 	<ul style="list-style-type: none"> Four socket Opteron processors 128GB of DDR2 memory 3.5" SAS internal HDD Ready RAID and Ready RSA Trusted Platform Module Standard TOE 	<ul style="list-style-type: none"> Dual socket Opteron processors 32 GB of DDR2 memory SAS HDD technology TOE NIC solution High speed enablement Supports the new SIO blade 	<ul style="list-style-type: none"> Four socket Opteron processors 64GB of DDR2 memory 2 SAS HDDs and RAID TOE NIC solution High speed enablement Supports the new SIO blade 4 ethernet ports
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LSDYNA

- Performance characteristics
 - benchmarks:
 - Neon: 230K; 20ms; 4-way
 - rNeon: 550K; 30ms; 4-way
 - 3Car: 1.2M; 10ms; 4-way
 - standard benchmarks: www.topcrunch.org
 - cache friendly (follows SPECfp)
 - similar performance on POWER5, Opteron and XEON
 - JS21 offers potential for excellent price/performance for AIX customers
 - scales well with clusters
 - high performance network now the norm
- IBM solutions
 - when AIX is the most important factor
 - System p JS21; 2.5GHz 4-way nodes; 8GBmem; 1 internal drive; Myrinet preferred
 - System p5 575+; 1.9GHz 16-way nodes; 16GBmem; 2 internal drives; HPS
 - when price/performance is most important factor
 - System x HS21; 3.00 GHz 4-way blades; 8GBmem, 1 internal drive; Myrinet or IB
 - System x 3550; 3.00 GHz 4-way nodes; 8GBmem, 1-2 internal drives; Myrinet or IB

Performance (relative to x336, higher is better)

Benchmark	x336/3.6	WC/3.0	ls20/2.2	js21/2.5
Neon	1.0	1.0	1.0	1.0
rNeon	1.5	1.2	0.8	1.5
3Car	1.4	1.1	0.9	1.4

LSTC
Livermore Software Technology Corp.

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LS-DYNA comparison: dual-core vs. quad-core

Sockets	dual-core 3.0 GHz	quad-core 2.66 GHz
2 sockets	~41000	~34000
4 sockets	~21000	~18500
8 sockets	~11500	-

April, 2007 testing, 3-car model, 795k elements, 150 msec
IBM x3550 3.0 GHz Xeon 5160 "Woodcrest"
IBM x3550 2.66 Xeon X5355 "Clovertown"

■ dual-core 3.0 GHz
■ quad-core 2.66 GHz

ELAPSED(sec)

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CAE Server Solutions

- No dominant server choice for all CAE applications
 - System p
 - strength of AIX
 - industry leading performance for many problems
 - well balanced performance for wide variety of simulation
 - System x
 - economics and flexibilities of open standards
 - extensive application portfolio
 - typically excellent price/performance




- No dominant server strategy for CAE customers
 - General purpose CAE servers
 - System p typically offers best performance for variety of applications
 - System x typically offer best price/performance
 - Application specific CAE servers
 - complex landscape which is always in flux

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Scalable Parallel I/O: General Parallel File System (GPFS)

- **NFS**
 - Client-server file systems have server bottleneck and protocol overhead
- **SAN**
 - SAN with a single metadata server have potential bottleneck
- **GPFS**
 - **General Purpose**
 - Any node can read from or write to any of the disks
 - The entire cluster can be administered from a single node
 - Supports Linux, AIX and mixed clusters
 - **High Performance**
 - Has provided 15GB/s to a single node and 100GB/s against a single file
 - *GPFS is not a client-server file system and has much lower protocol overhead*
 - All system data & metadata is equally accessible from all nodes
 - All data & metadata flows between the disks and nodes in parallel
 - **Scalability**
 - Currently supports 100s of nodes and 200+TB of storage over LAN or HPS (more by special bid)
 - **Reliability**
 - Parallel operation means no single point of failure
 - One large research customer reported 100% uptime for GPFS for an entire year

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Members of IBM's CAE Team (1)

<u>Nick Alsopp</u>	ABAQUS, CFX	Many years experience in HPC and with HKS
<u>Balaji Atyam</u>	ANSYS, LMS, Madymo	Application Support USA
<u>John Bauer</u>	HPC I/O Libraries	Original developer of EIEIO libraries, 19 years HPC experience
<u>Steve Behling</u>	STAR-CD	13 years experience with STAR-CD source code
<u>Achim Bömelburg</u>	Permas, CAE Team	16 years experience with automotive customers
<u>David Wei Chen</u>	Detroit CAE Team	9 years working with automotive users in Detroit
<u>Greg Clifford</u>	Leader CAE Practice	20 years working with CAE customers and ISVs
<u>Martin Feyereisen</u>	Pam-Crash, LS-Dyna	Considered a member of the ESI development team
<u>John Hague</u>	VECTIS	ACTC Team UK

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Members of IBM's CAE Team (2)

<u>Holger Holthoff</u>	RADIOSS, AVL	11 years experience in parallel computing on IBM platforms
<u>Nobuhiko Kudanami</u>	Tokyo CAE Team	Working with automotive users in Tokyo
<u>Guangye Li</u>	LS-DYNA	Extensive experience with LS-DYNA on Linux
<u>Doug Petesch</u>	NASTRAN, AMLS	15 years experience working with MSC and customers
<u>Hari Reddy</u>	FLUENT, PowerFLOW	6 years experience with FLUENT, experience with various CAE codes
<u>A. Sugavanam</u>	PowerFLOW, CEM	Many years experience with NASA and CAE codes
<u>Erling Weibust</u>	CAE Team Sweden	19 years working with technical customers in Nordic region
<u>Jeff Zais</u>	Leader CAE technical team	Key player in the 1999 success of MPP-DYNA
...		

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The IBM Value

- Experienced HPC applications team
- Worldwide customers
 - Longstanding relations with key application vendors
- Full range of computing solutions
 - POWER6 to Linux Clusters
 - Storage Solutions
- Presence of IBM
 - Stable company, growing in technical computing
 - Able to offer complementary solutions for storage and the desktop.



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